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ANALOG LAYOUT GENERATION FOR PERFORMANCE AND MANUFACTURABILITY by Koen Lampaert Katholieke Universiteit Leuven Georges Gielen Katholieke Universiteit Leuven and Willy Sansen Katholieke Universiteit Leuven SPRINGER SCIENCE+BUSINESS MEDIA, LLC . A C.I.P. Catalogue record for this book is available from the Library of Congress.

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In Analog Layout Generation for Performance and Manufacturability, the authors propose a performance driven layout strategy to overcome this problem. In this methodology, the layout tools are driven by performance constraints, such that the final layout, with parasitic effects, still satisfies the specifications of the circuit.

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more layout iterations could be needed. In Analog Layout Generation for Performance and Manufacturability, the authors propose a performance driven layout strategy to overcome this problem. In this methodology, the layout tools are driven by performance constraints, such that the final layout, with parasitic effects, still satisfies the ...

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Analog layout can be broken down into three phases: device generation, device placement and routing. In the device generation phase, three types of devices are

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